

FIG. 1A

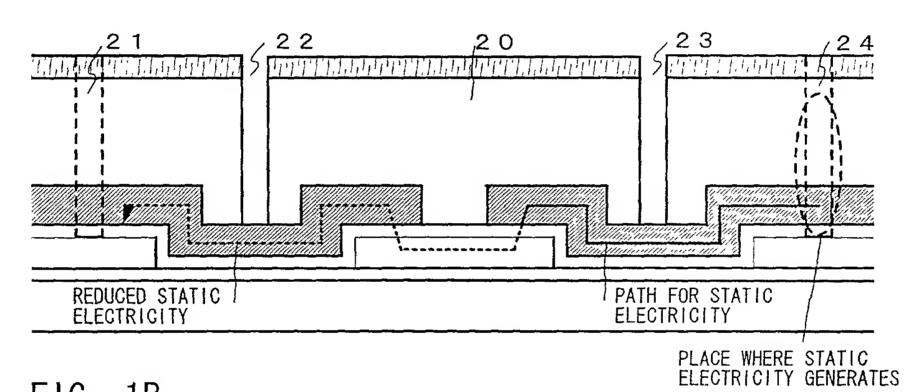


FIG. 1B

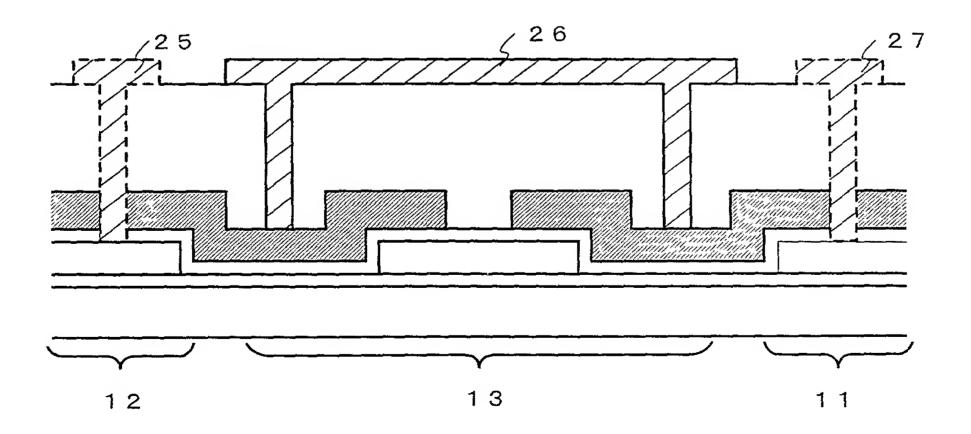
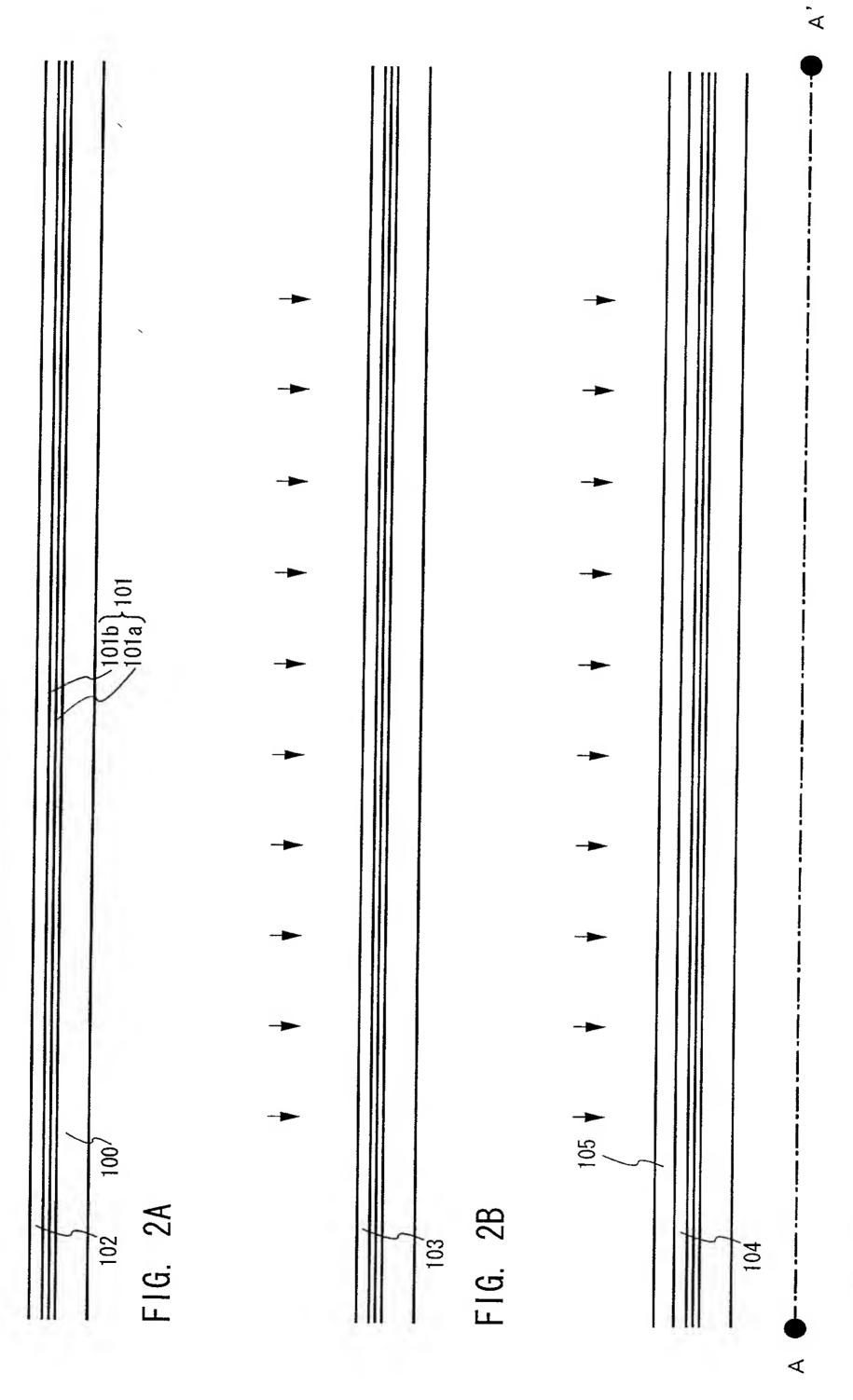
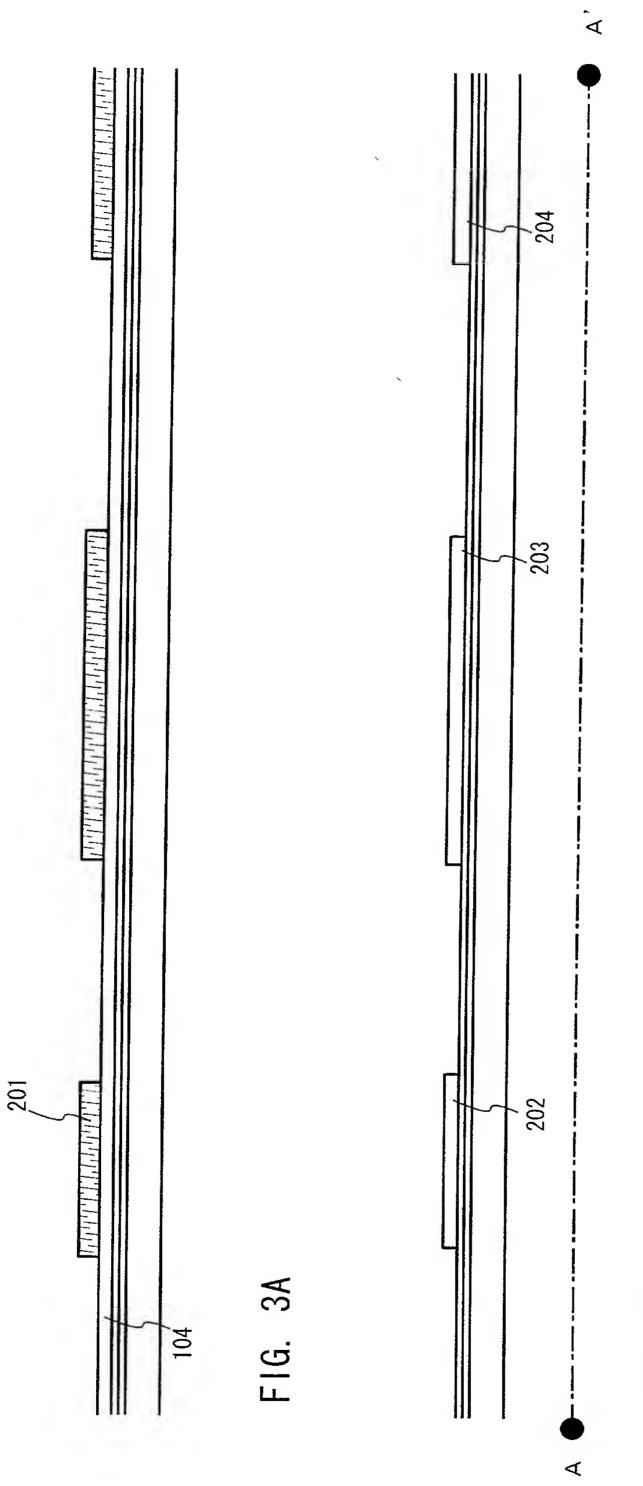


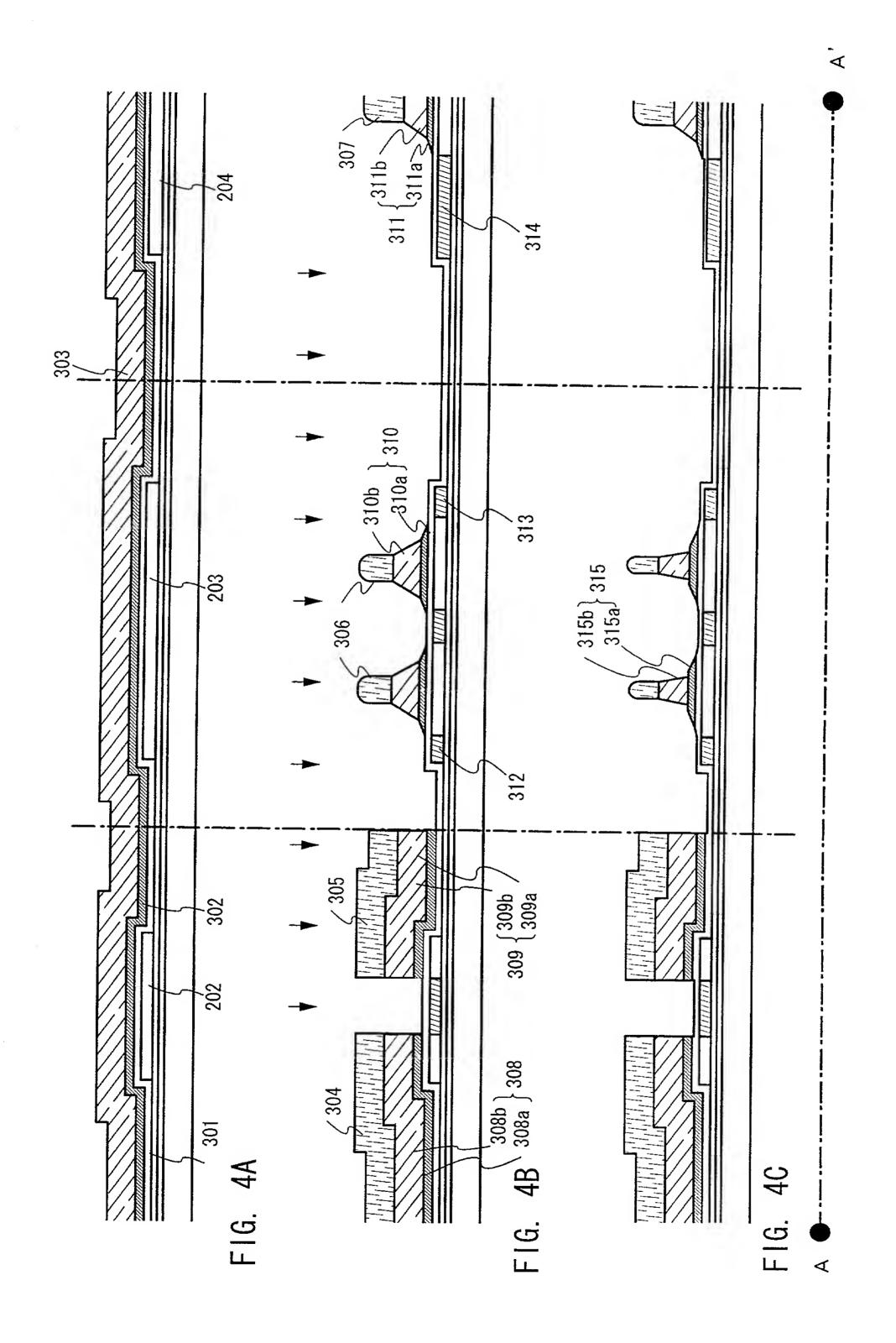
FIG. 1C



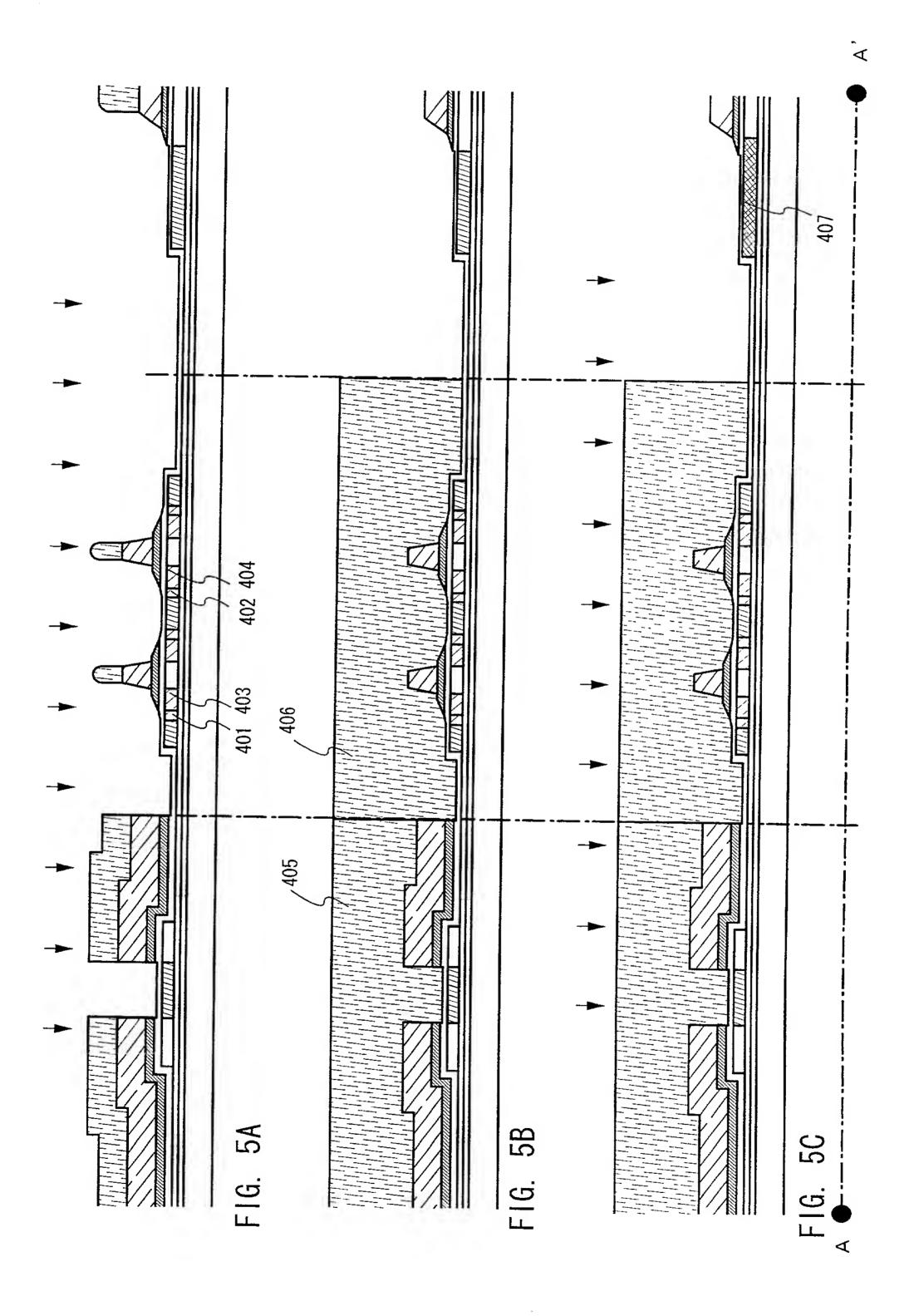
F1G. 20

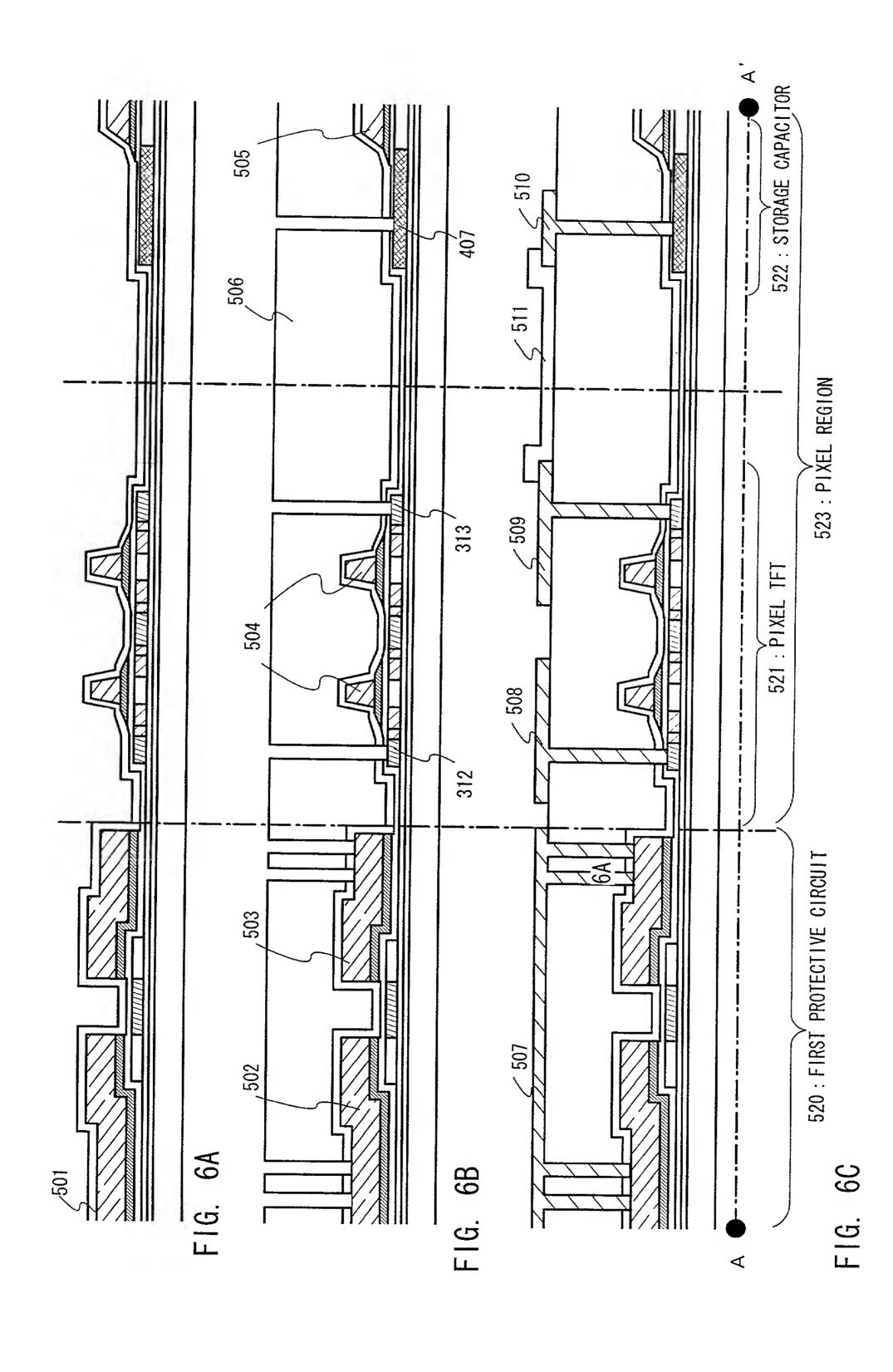


F1G. 3B



*Kindle Co.





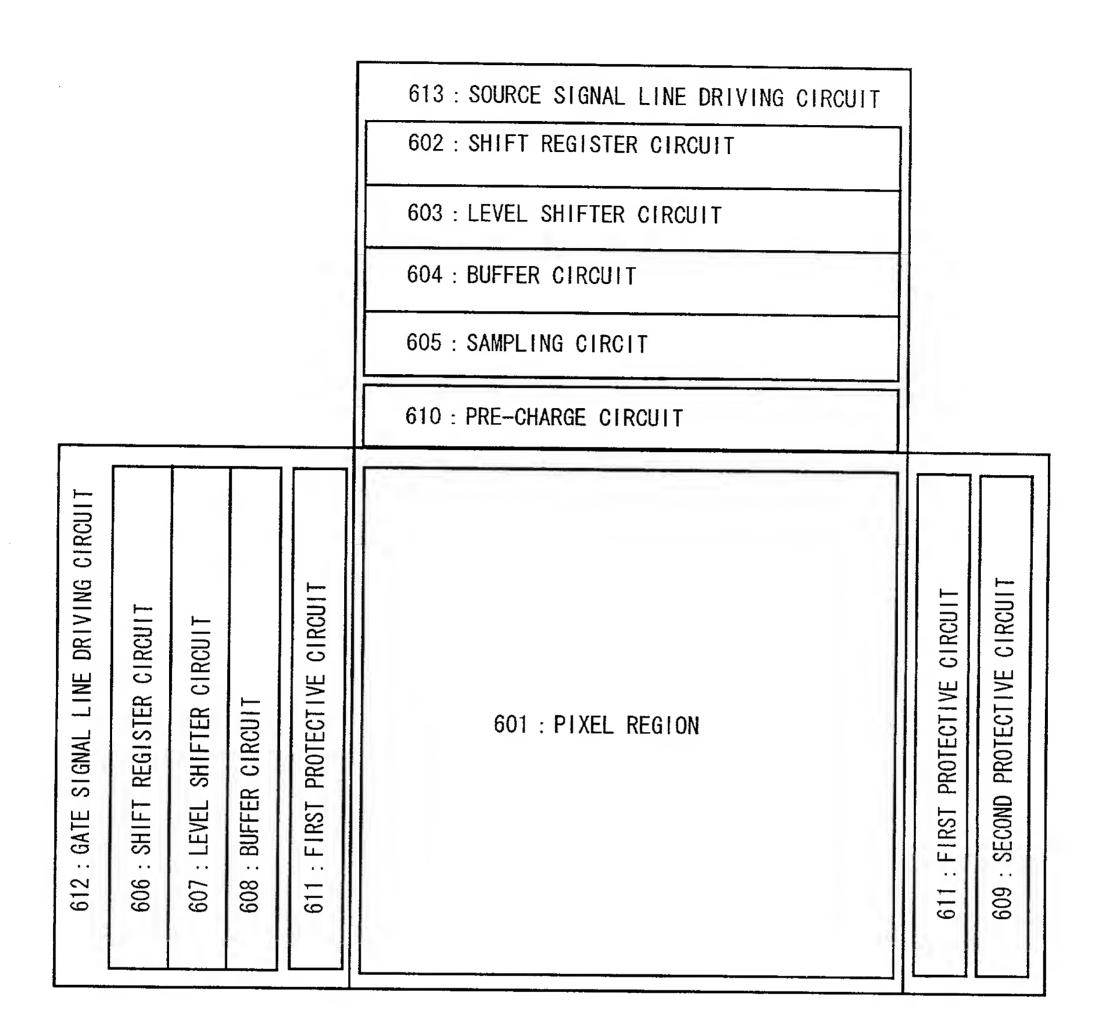


FIG. 7

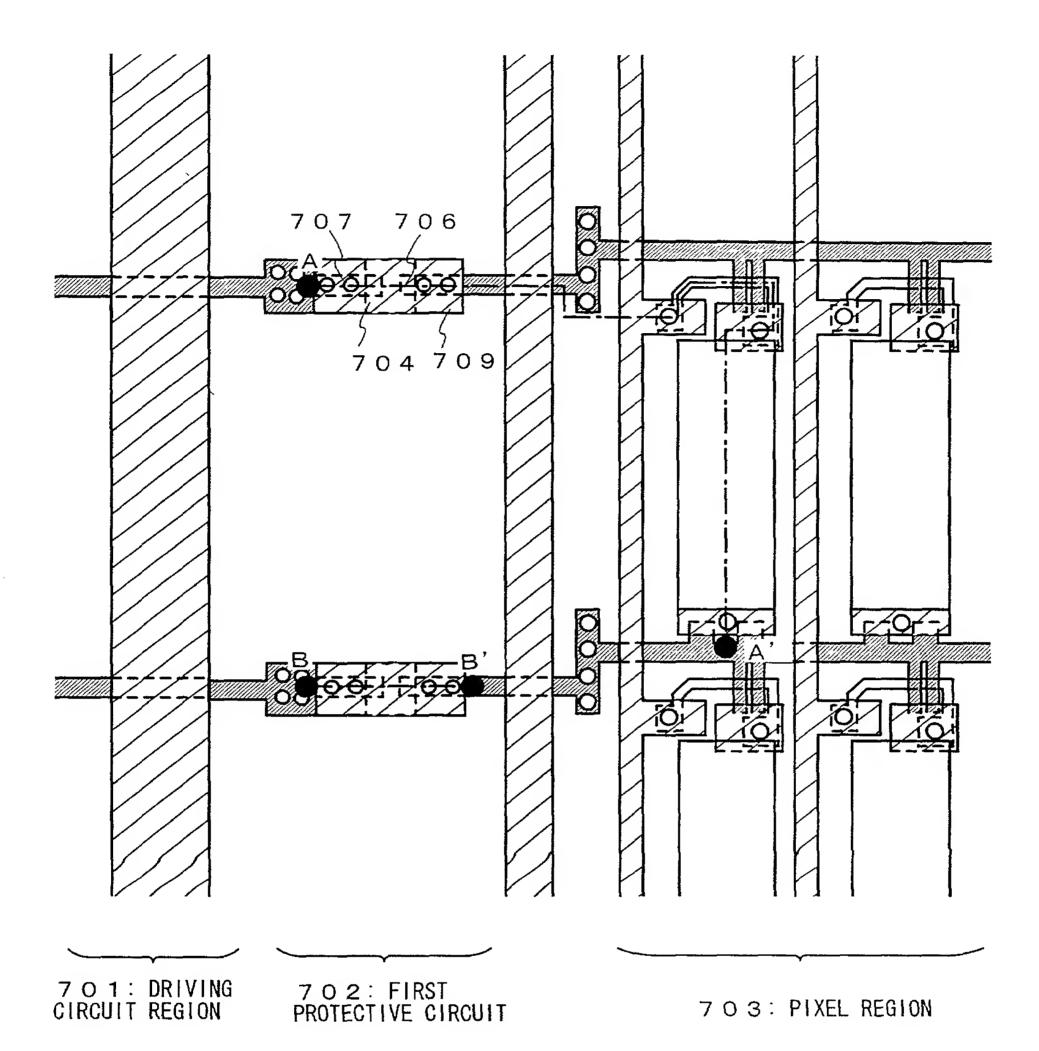


FIG. 8A

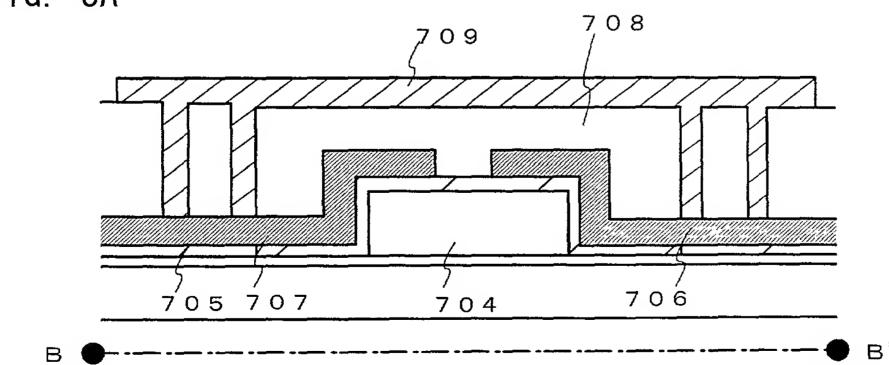


FIG. 8B

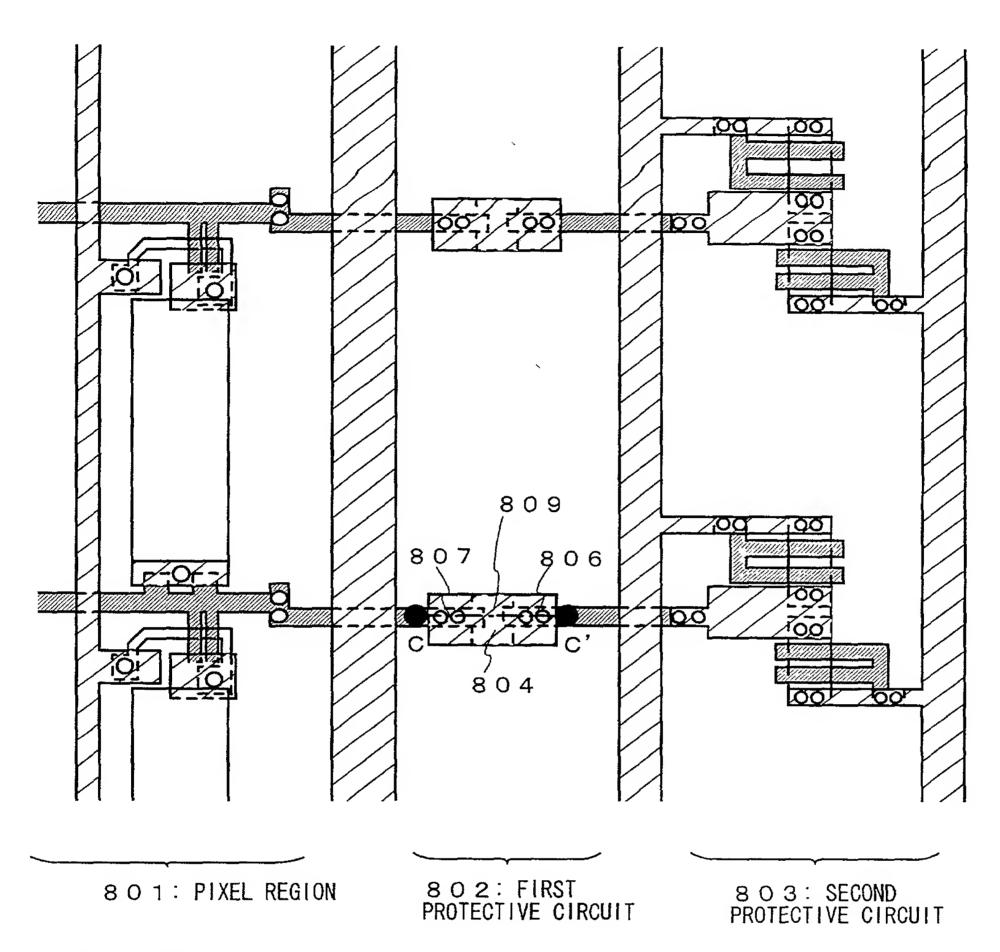


FIG. 9A

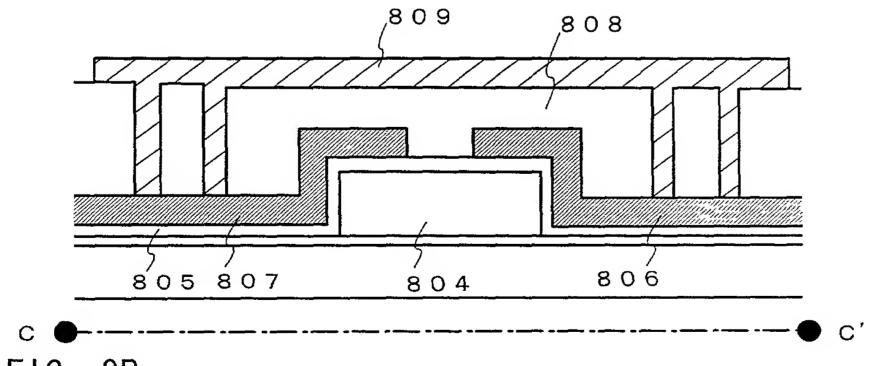


FIG. 9B

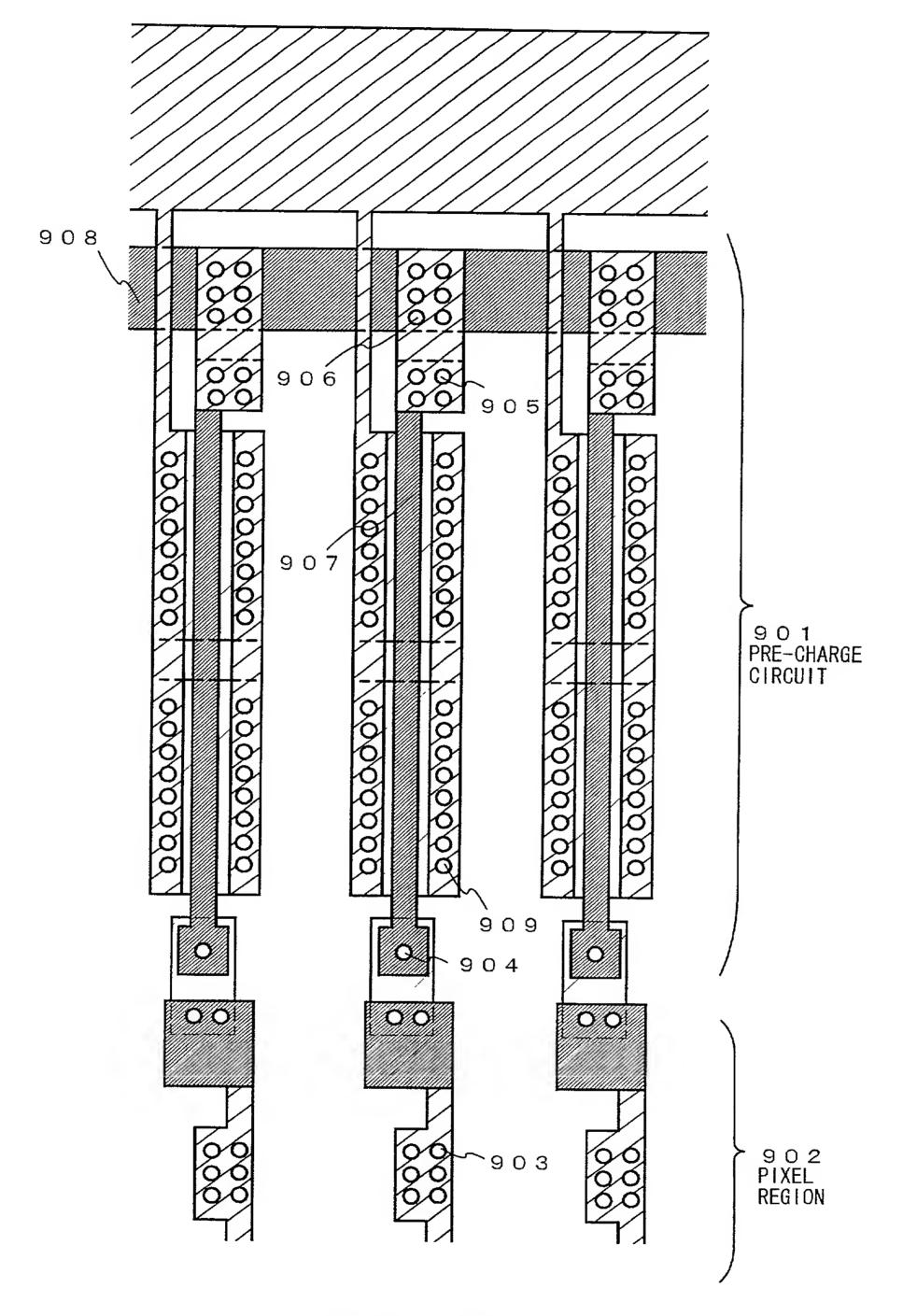


FIG. 10

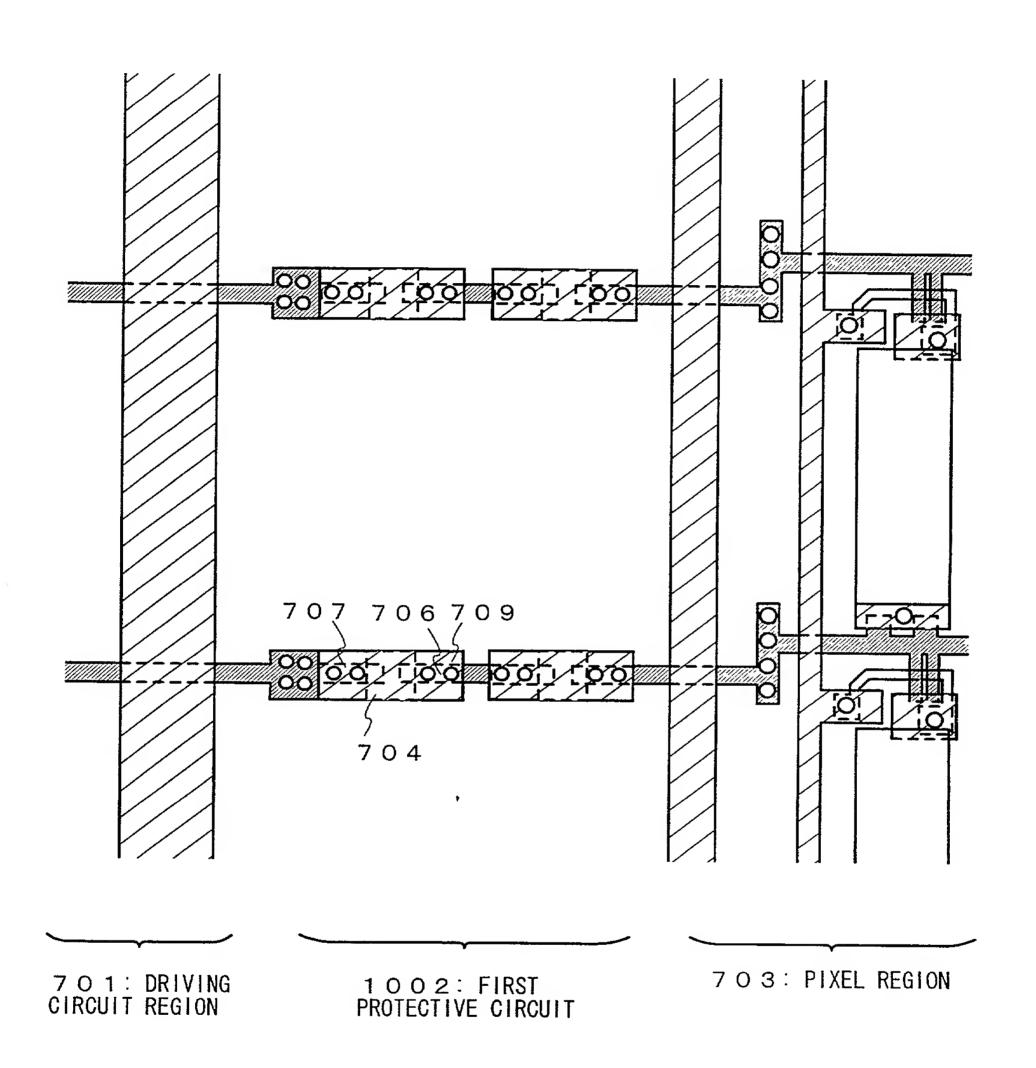


FIG. 11

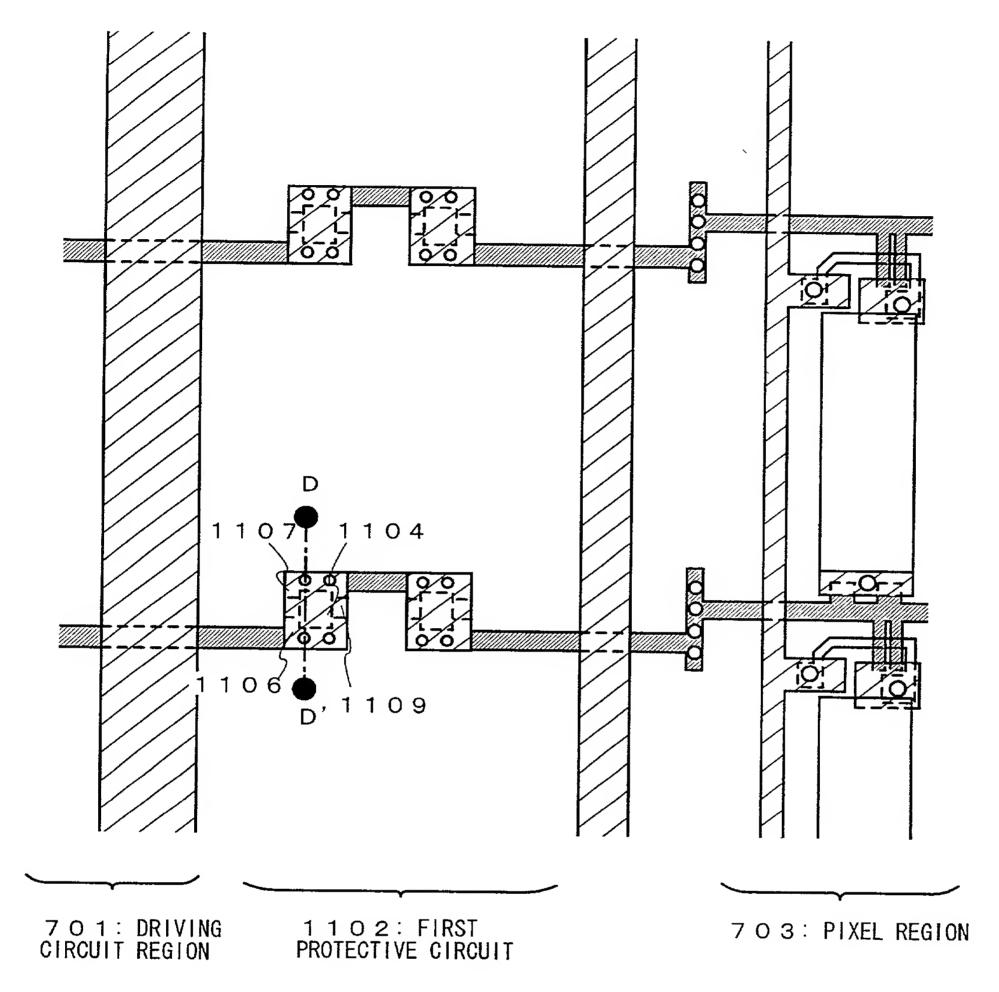


FIG. 12A

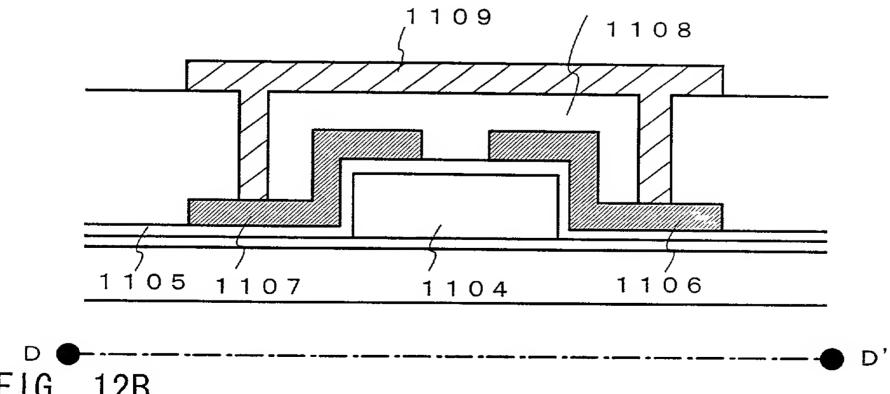
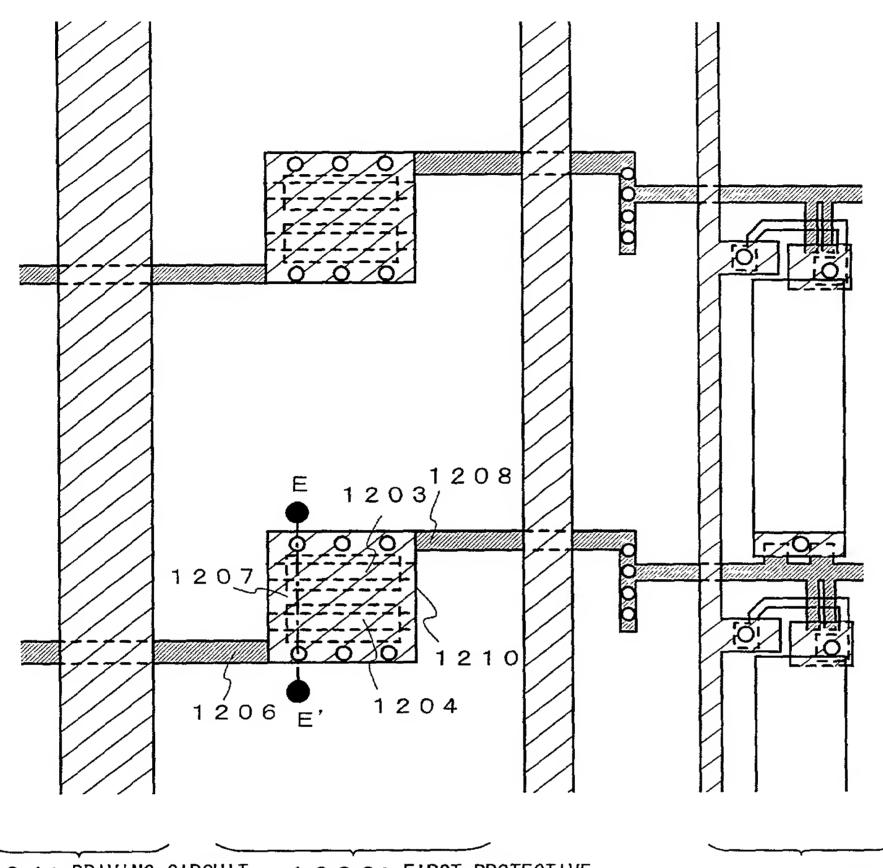


FIG. 12B



7 O 1: DRIVING CIRCUIT REGION 1 2 0 2: FIRST PROTECTIVE CIRCUIT 7 O 3: PIXEL REGION

FIG. 13A

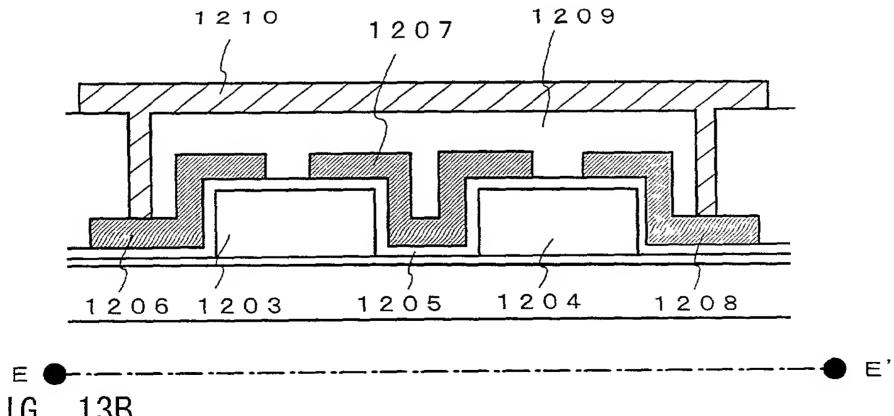
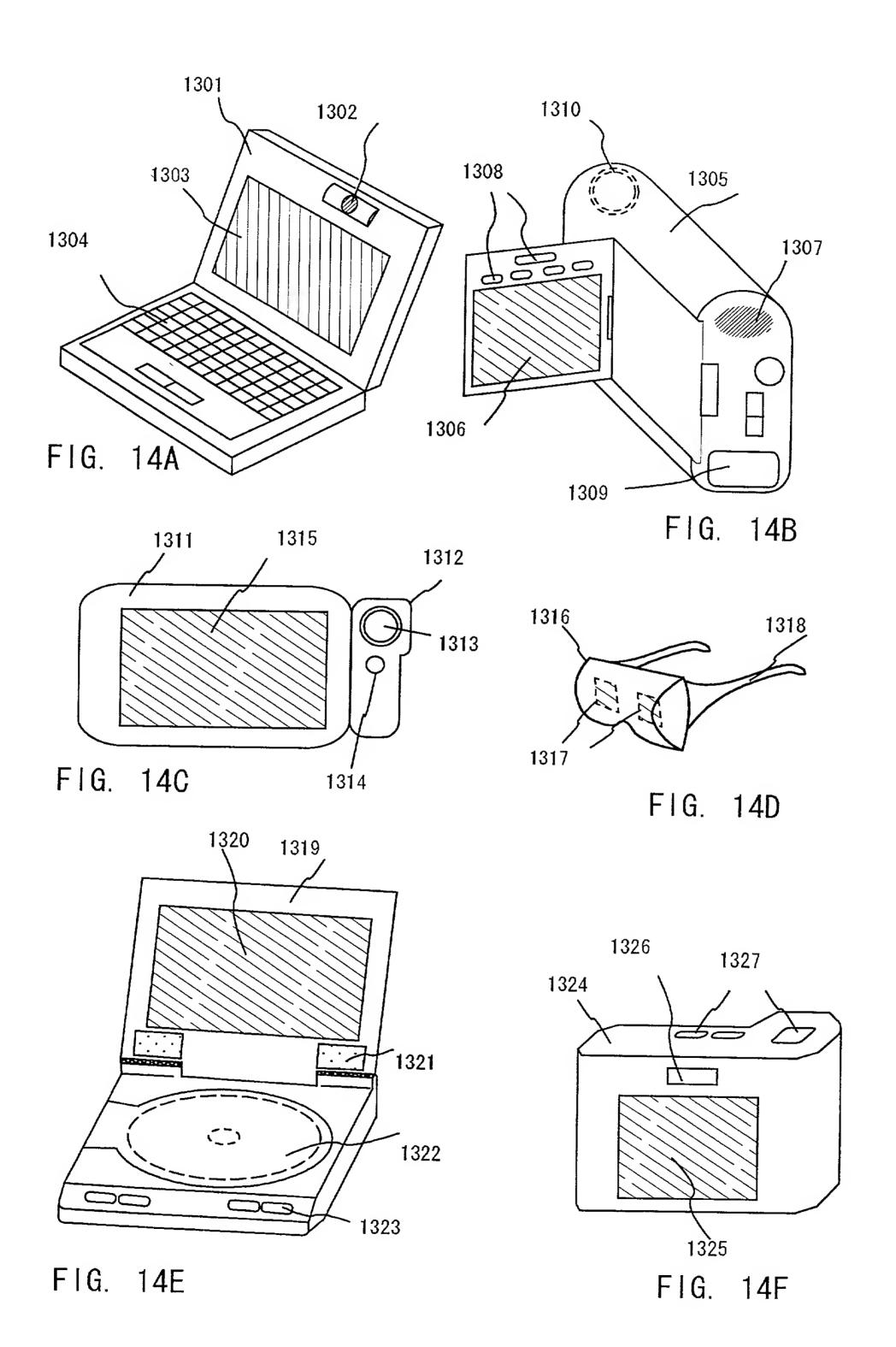


FIG. 13B



- KARANA

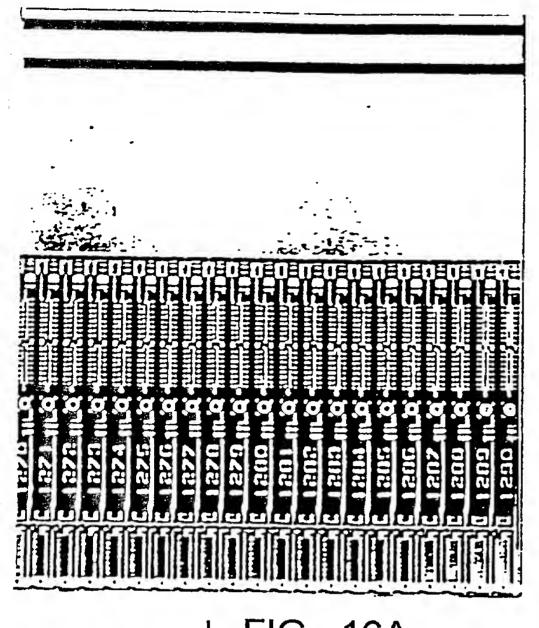
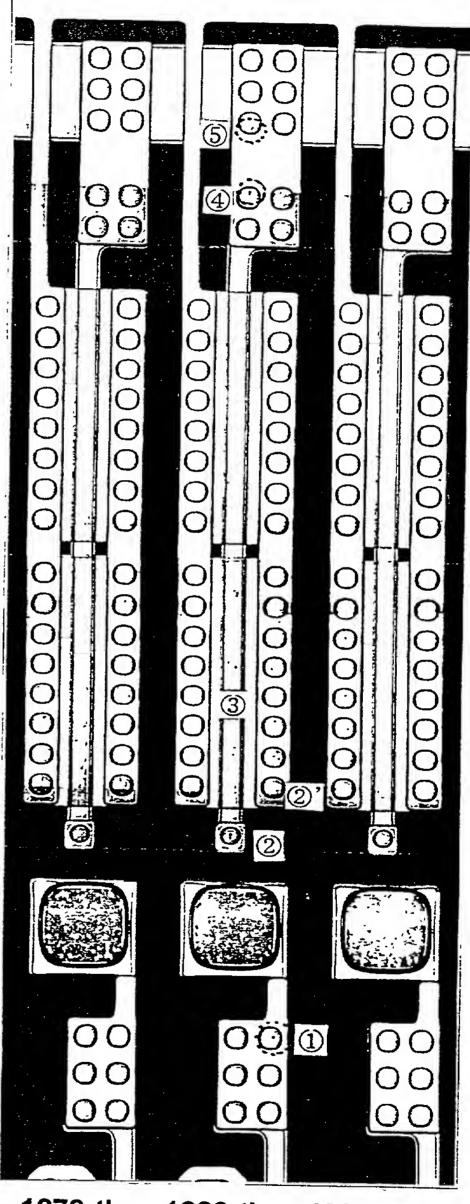
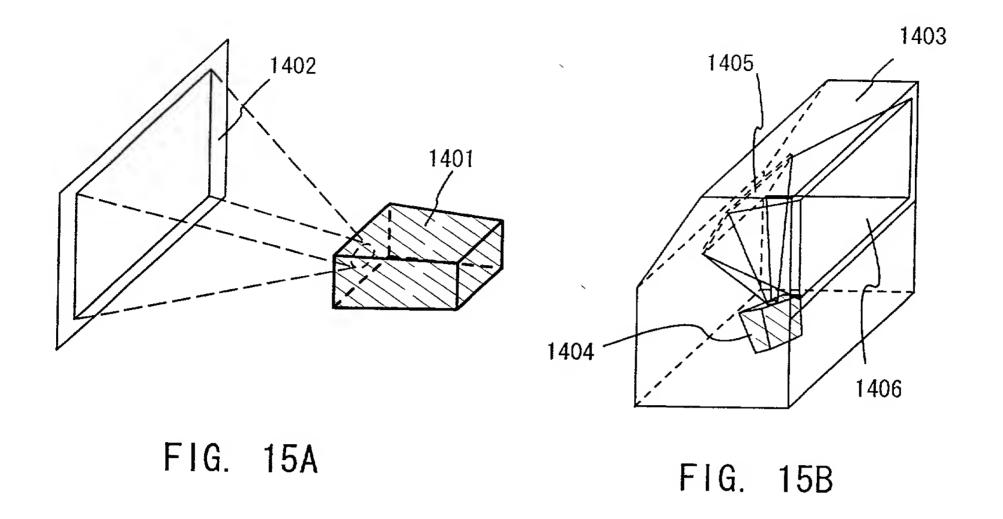


FIG. 16A



1279-th 1280-th 1281-th

FIG. 16B



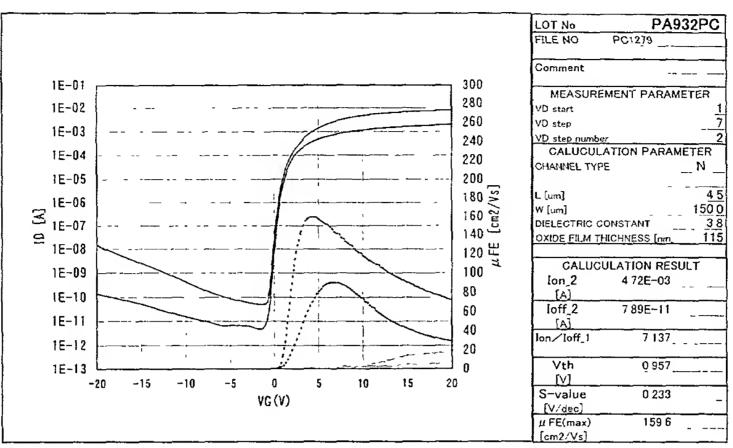


FIG. 17A

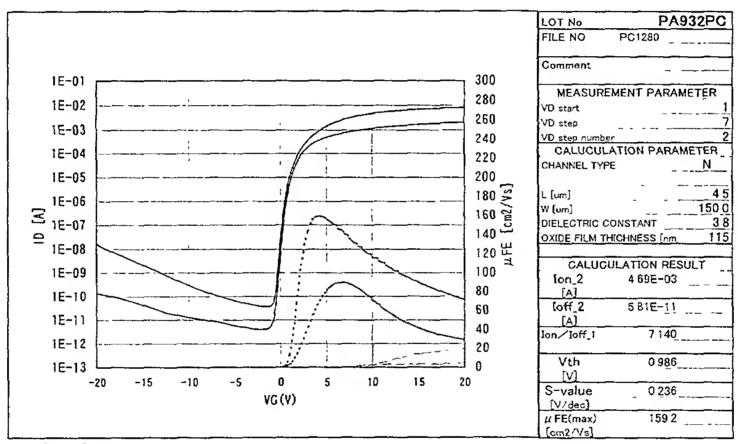


FIG. 17B

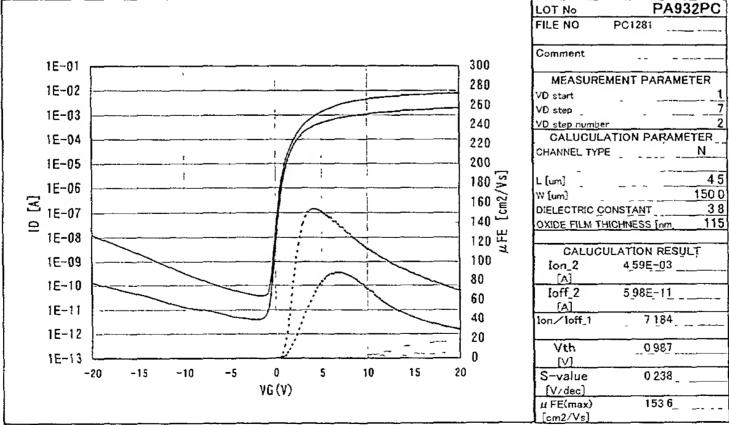


FIG. 17C

				1712 : SOURCE SIGNAL LINE DRIVING CIRCUIT 1702 : SHIFT REGISTER CIRCUIT 1703 : LEVEL SHIFTER CIRCUIT 1704 : BUFFER CIRCUIT 1705 : SAMPLING CIRCUIT	
1711 : GATE SIGNAL LINE DRIVING CIRCUIT	1707 : SHIFT REGISTER CIRCUIT	1708 : LEVEL SHIFTER CIRCUIT	1709 : BUFFER CIRCUIT	1701 : PIXEL REGION	1710 : PROTECTIVE CIRCUIT

FIG. 18

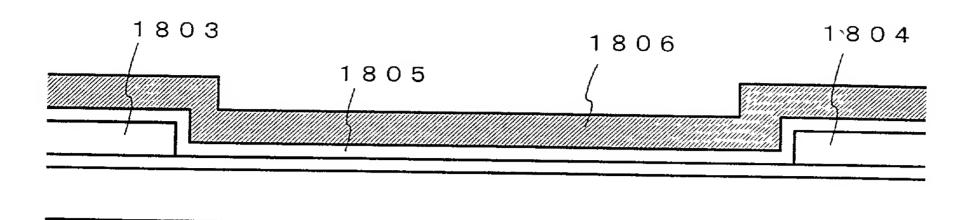
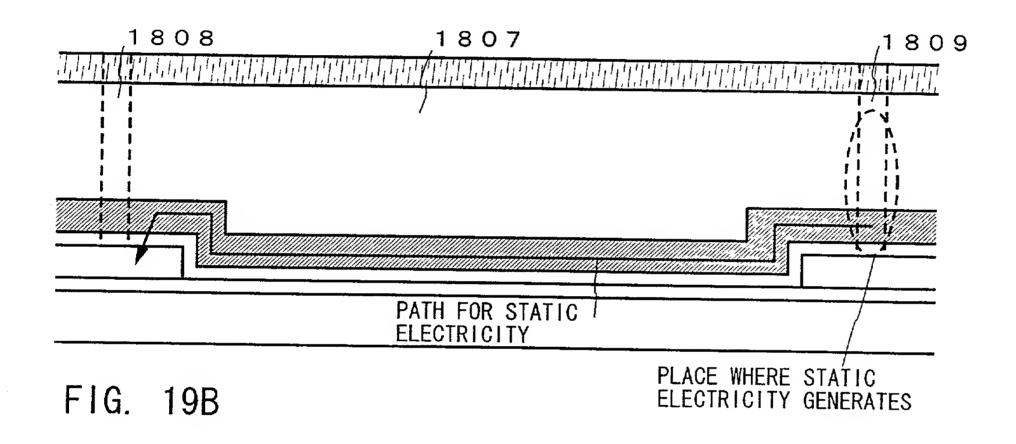


FIG. 19A



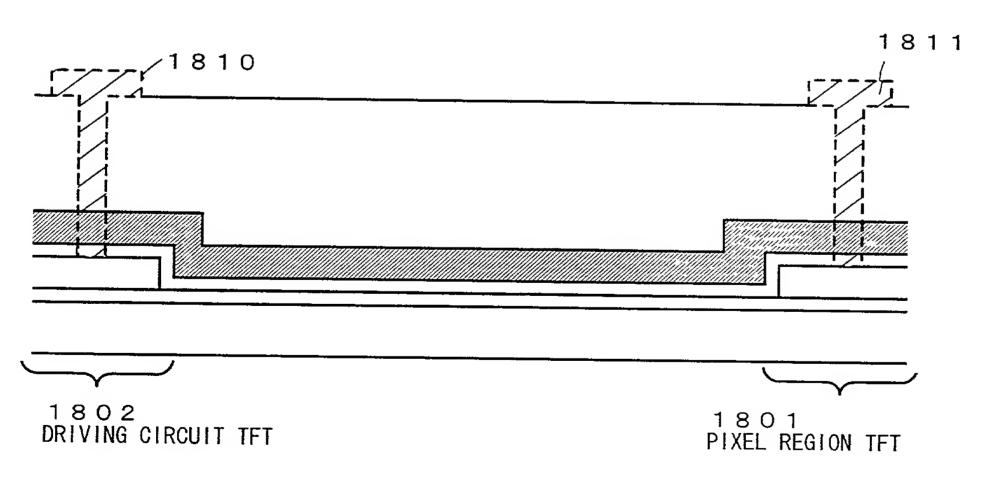


FIG. 19C

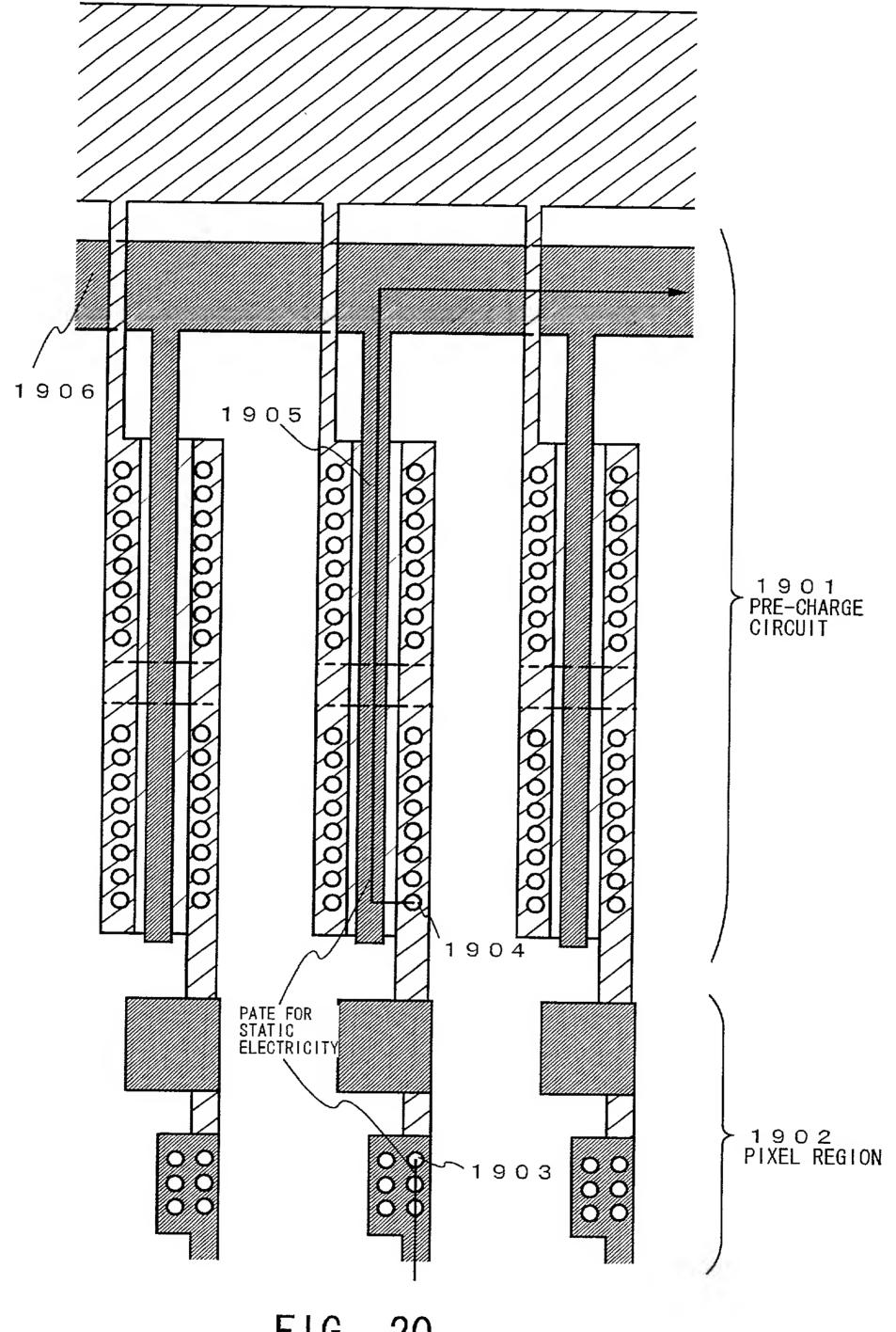


FIG. 20